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LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Ji Ung Lee et al.	FILING DATE Filed Herewith 2/5/02
				GROUP 2825 Filed Herewith	10/01/2015 JCS96 US 2-10

  

U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate	
NY ↑ ↓ NY	AA	5,482,870	01/09/98	Inoue			
	AB	5,372,973	12/13/94	Doan et al.			
	AC	5,229,331	07/20/93	Doan et al.			
	AD	5,210,472	05/11/93	Casper et al.			
	AE	4,988,838	01/29/91	Huang et al.			
	AF	6,057,555	5/2/00	Reedy et al.			
	AG	5,710,478	1/20/98	Kanamaru et al.			
	AH	6,020,683	2/1/00	Cathey, Jr. et al.			
	AI	6,249,327 B1	6/19/01	Murade et al. ✓			
	AK						

  

FOREIGN PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
NY ↑ ↓ NY	AL	5-114734	05/07/93	Japan		X	
	AM	3-194937	08/26/91	Japan		X	
	AN	3-159250	07/09/91	Japan		X	
	AO	A-2-143462	11/24/88	Japan		X	
	AP	61-252687	11/10/88	Japan		X	
	AQ	A-57-85282	11/17/80	Japan		X	

  

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)		
NY   NY	AR   AS  AT	Polycrystalline Silicon Thin Film Transistor Incorporating a Semi-Insulating Field Plate for High Voltage Circuitry on Glass,  F.J. Clough, E.M.S. Narayanan, Y. Chen, W. Eccleston, and W.J. Mäna, Appl. Phys. Lett. 71 - 10/06/97, pages 2002-2004, 1997 American Institute of Physics.  Geometry Dependence of the Transport Parameters in Field Effect Transistors Made From Amorphous Silicon, S. Grisp, Mat. Res. Soc. Symp. Proc. 149, pages 283-288, 1989 Materials Research Society.  

  

EXAMINER V. Juston	DATE CONSIDERED 02/03/04
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.